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# Informacije o proizvodu (EN)

SEAGATE hard disk 4TB ST4000DM004





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SATA Product Manual

# **Standard models**

ST8000DM004 ST6000DM003 ST4000DM004 ST3000DM007 ST2000DM005

100805918, Rev. N July 2020

# **Document Revision History**

Revision	Date	Pages affected	
Rev. A	08/05/2016	Initial release.	
Rev. B	09/07/2016	fc, 6-8, 11-13, 16-18 & 29-30: Added 5TB model, specs & references 9 & 12: Updated Avg seek times to 15.0 ms 13: DC power - Standby/Sleep edits) 18: Added Korean text for Class B device warning	
Rev. C	11/28/2016	fc: New BarraCuda logo fc, 6, 15, 21 & 24: Changed name to BarraCuda fc, 6-7, 11-13, 16-17 & 29-30: Remove 5TB model & specs 7, 9 & 11: Revised Data transfer rates 7, 9 & 12: Removed Seek times from all capacities 8-9 & 16: Changed Op shock to 80Gs (read) / 70 Gs (write) at 2ms 13: Section 2.8.1 Power Consumption & DC Power table edits 14: Revised Section 2.8.4 Power-management modes 18: Revised to MSIP Korean text Class B device warning 22-23: Revised Figures 2 & 3 mechanical drawings	
Rev. D	02/21/2017	<ul> <li>8 &amp; 10: Added Power-off/Flush Command note after Tables 1 &amp; 2.</li> <li>8 &amp; 10: Revised Rated Workload statements</li> <li>18: Added Section 2.11.1 Data loss under power interruption with write cache enabled</li> </ul>	
Rev. E	06/22/2017	9 & 12: Updated Max Height to 20.20mm/0.795 in. 22: Updated Max Height in Figure 2: Mounting dimensions (1 and 2-disk models)	
Rev. F	09/28/2017	18-21: Updated Safety, EMC, FCC & RoHS Sections 2.12 through 2.13.3, per Compliance Council	
Rev. G	11/03/2017	7 & 9: Updated 6TB & 3TB Maximum data transfer rate to 185 MB/s 15: Added Figure 1: Location of the HDA temperature check point 21: Updated Table 6: Taiwan - Restricted Substances - Unit row headers = HDD & PCBA 23-24: Corrected fastener penetration depth to 0.140 inches in Section 3.4 & Figures 3 & 4	
Rev. H	04/13/2018	18: Added Regulatory Model Number	
Rev. J	06/29/2018	<ul> <li>22: Updated Table 8: Taiwan - Restricted Substances to reflect latest requirements per BSMI</li> <li>31: Corrected Word 84 value to 4163<sub>H</sub></li> </ul>	
Rev. K	05/28/2019	6: Removed TGMR Bullet bc: Removed APAC & EMEA addresses	
Rev. L	07/10/2019	8 & 10: Revised Non-recoverable read errors to 10E14	
Rev. M	05/13/2020	<ul> <li>5: Updated Seagate Technology Support Services page</li> <li>11: Section 2.4 Recording and interface technology, changed to - Recording technology = SMR</li> <li>18: Replaced Regulatory Compliance and Safety information with URL and instructions</li> <li>bc: Updated US HQ address</li> </ul>	
Rev. N	07-08-2020	9: Revised 3TB & 2TB heads & disks 9 & 12: Revised 2TB model weight 9 & 12: Revised Power-on & Standby to ready for 2TB model 10 & 17: Revised Acoustics for 2TB model 14: Revised to 5VDC & 12VDC	

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Publication number: 100805918, Rev. N July 2020

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# 1.0 Introduction

This manual describes the functional, mechanical and interface specifications for the following Seagate® BarraCuda® model drives:

ST8000DM004 ST6000DM003 ST4000DM004 ST3000DM007 ST2000DM005

These drives provide the following key features:

- · Compliant with RoHS requirements in China and Europe.
- High instantaneous (burst) data-transfer rates (up to 600MB per second).
- Native Command Queuing with command ordering to increase performance in demanding applications.
- Quiet operation.
- SeaTools diagnostic software performs a drive self-test that eliminates unnecessary drive returns.
- · State-of-the-art cache and on-the-fly error-correction algorithms.
- Support for S.M.A.R.T. drive monitoring and reporting.
- Supports latching SATA cables and connectors.
- Worldwide Name (WWN) capability uniquely identifies the drive.

# 1.1 About the SATA interface

The Serial ATA (SATA) interface provides several advantages over the traditional (parallel) ATA interface. The primary advantages include:

- Easy installation and configuration with true plug-and-play connectivity. It is not necessary to set any jumpers or other configuration options.
- Thinner and more flexible cabling for improved enclosure airflow and ease of installation.
- Scalability to higher performance levels.

In addition, SATA makes the transition from parallel ATA easy by providing legacy software support. SATA was designed to allow users to install a SATA host adapter and SATA disk drive in the current system and expect all of the existing applications to work as normal.

The SATA interface connects each disk drive in a point-to-point configuration with the SATA host adapter. There is no master/slave relationship with SATA devices like there is with parallel ATA. If two drives are attached on one SATA host adapter, the host operating system views the two devices as if they were both "masters" on two separate ports. This essentially means both drives behave as if they are Device 0 (master) devices.

The SATA host adapter and drive share the function of emulating parallel ATA device behavior to provide backward compatibility with existing host systems and software. The Command and Control Block registers, PIO and DMA data transfers, resets, and interrupts are all emulated.

The SATA host adapter contains a set of registers that shadow the contents of the traditional device registers, referred to as the Shadow Register Block. All SATA devices behave like Device 0 devices. For additional information about how SATA emulates parallel ATA, refer to the "Serial ATA International Organization: Serial ATA Revision 3.0". The specification can be downloaded from <u>www.sata-io.org</u>.

The host adapter may, optionally, emulate a master/slave environment to host software where two devices on separate SATA ports are represented to host software as a Device 0 (master) and Device 1 (slave) accessed at the same set of host bus addresses. A host adapter that emulates a master/slave
environment manages two sets of shadow registers. This is not a typical SATA environment.

# 2.0 Drive Specifications

Unless otherwise noted, all specifications are measured under ambient conditions, at 25°C, and nominal power. For convenience, the phrases *the drive* and *this drive* are used throughout this manual to indicate the following drive models:

ST8000DM004 ST6000DM003 ST4000DM004 ST3000DM007 ST2000DM005

# 2.1 Specification summary tables

The specifications listed in **Table 1** are for quick reference. For details on specification measurement or definition, refer to the appropriate section of this manual.

Table 1	Drive specifications summar	v for 8TB & 6TB model
I MUICI		

Drive Specification*	ST8000DM004	ST6000DM003	
Formatted capacity (512 bytes/sector)**	8000GB (8TB)	6000GB (6TB)	
Guaranteed sectors	15,628,053,168	11,721,045,168	
Heads	8	6	
Disks	4	3	
Bytes per sector (4K physical emulated at 512-byte sectors)	4	1096	
Default sectors per track		63	
Default read/write heads		16	
Default cylinders	16	5,383	
Recording density (max)	229	4 kB/in	
Track density (avg)	540 k	tracks/in	
Areal density (avg)	1203	3 Gb/in <sup>2</sup>	
SATA interface transfer rate	600	) MB/s	
Maximum data transfer rate	190 MB/s	185 MB/s	
ATA data-transfer modes supported	PIO modes: 0 to 4 Multiword DMA modes: 0 to 2 Ultra DMA modes 0 to 6		
Cache buffer	256MB		
Height (max)	26.1mm / 1.028 in		
Width (max)	101.6mm (± 0.25) / 4.0 in (± 0.010)		
Length (max)	146.99m	m / 5.787 in	
Weight (typical)	630g / 1.389 lb	610g / 1.345 lb	
Average latency	6.	0 ms	
Power-on to ready (typ)	1	5.0s	
Standby to ready (typ)	1	5.0s	
Startup current (typical) 12V	2	2.0A	
Voltage tolerance (including noise)	5V ±5% 12V ±10%		
Non-Operating (Ambient °C)	-40° to 70°		
Operating ambient temperature (min °C)	0°		
Operating temperature (drive case max °C)	60° <sup>†</sup>		
Temperature gradient	20°C per hour max (operating) 30°C per hour max (non-operating)		
Relative humidity	5% to 90% (operating) 5% to 95% (non-operating)		
Relative humidity gradient (max)	tive humidity gradient (max) 30% per hour		

Table 1	Drive specifications summar	y for 8TB & 6TB model (continued)

Drive Specification*	ST8000DM004	ST6000DM003		
Wet bulb temperature (max)		30°C max (operating) 40°C max (non-operating)		
Altitude, operating		to 3048m o 10,000 ft)		
Altitude, non-operating (below mean sea level, max)		o12,192m o 40,000+ ft)		
Operational shock (max)	80 Gs (read) / 70	Gs (write) at 2ms		
Non-operational shock (max)	300 Gs	s at 2ms		
Vibration, operating	22Hz to 35	;, Limited displacement 0Hz: 0.50 Gs 00Hz: 0.25 Gs		
Vibration, non-operating	22Hz to 35	5Hz to 22Hz: 3.0 Gs 22Hz to 350Hz: 3.0 Gs 350Hz to 500Hz: 3.0 Gs		
Drive acoustics, sound power				
Idle***	2.3 bels (typical) 2.5 bels (max)			
Seek	2.5 bels (typical) 2.6 bels (max)			
Non-recoverable read errors	1 per 10 <sup>1</sup>	<sup>4</sup> bits read		
Rated workload	Average annualized workload rating: <55 TB/year. The specifications for the product assumes the I/O workload does not exceed the averag annualized workload rate limit of 55 TB/year. Workloads exceeding the annualized rate n degrade and impact reliability as experienced by the particular application. The average annualized workload rate limit is in units of TB per calendar year.			
Warranty	page: http://www.seagate.com/support/warra	rranty". Users will be asked to provide the drive r) and country of purchase. The system will		
Load/unload cycles	600,000 at 25°C,	600,000 at 25°C, 50% rel. humidity		
Supports hotplug operation per the Serial ATA Revision 3.2 specification	Y	/es		

\* All specifications above are based on native configurations.

\*\* One GB equals one billion bytes and 1TB equals one trillion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

\*\*\* During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

 Seagate does not recommend operating at sustained case temperatures above 60°C. Operating at higher temperatures will reduce useful life of the product.

**Note** If the drive is powered-off before issuing flush cache command, in some instances, the end user data in the DRAM cache might not be committed to the disk.

## Table 2Drive specifications summary for 4TB, 3TB & 2TB model

Drive Specification*	ST4000DM004	ST3000DM007	ST2000DM005
Formatted capacity (512 bytes/sector)**	4000GB (4TB)	3000GB (3TB)	2000GB (2TB)
Guaranteed sectors	7,814,037,168	5,860,533,168	3,907,029,168
Heads	4	4/3	3/2
Disks		2	2/1
Bytes per sector (4K physical emulated at 512-byte sectors)		4096	
Default sectors per track		63	
Default read/write heads		16	
Default cylinders		16,383	
Recording density (max)		2294 kB/in	
Track density (avg)		540 ktracks/in	
Areal density (avg)		1203 Gb/in <sup>2</sup>	
SATA interface transfer rate		600 MB/s	
Maximum data transfer rate	190 MB/s	185 MB/s	190 MB/s
ATA data-transfer modes supported		PIO modes: 0 to 4 Multiword DMA modes: 0 to 2 Ultra DMA modes 0 to 6	
Cache buffer		256MB	
Height (max)		20.20mm / 0.795 in	
Width (max)		101.6mm (± 0.25) / 4.0 in (± 0.010)	
Length (max)		146.99mm / 5.787 in	
Weight (typical)	490g / 1.08 lb		
Average latency	6.0 ms		
Power-on to ready (typ)	10.0s		
Standby to ready (typ)	10.0s		
Startup current (typical) 12V		2.0A	
Voltage tolerance (including noise)	5V ±5% 12V ±10%		
Non-Operating (Ambient °C)		–40° to 70°	
Operating ambient temperature (min °C)		0°	
Operating temperature (drive case max °C)		60° <sup>†</sup>	
Temperature gradient		20°C per hour max (operating) 30°C per hour max (non-operating)	
Relative humidity		5% to 90% (operating) 5% to 95% (non-operating)	
Relative humidity gradient (max)		30% per hour	
Wet bulb temperature (max)	30°C max (operating) 40°C max (non-operating)		
Altitude, operating	-304m to 3048m (-1000 ft to 10,000 ft)		
Altitude, non-operating (below mean sea level, max)	-304m to12,192m (-1000 ft to 40,000+ ft)		
Operational shock (max)	80 Gs (read) / 70 Gs (write) at 2ms		
Non-operational shock (max)	300 Gs at 2ms 350 Gs at 2ms		
Vibration, operating	10Hz to 22Hz: 0.25 Gs, Limited displacement 22Hz to 350Hz: 0.50 Gs 350Hz to 500Hz: 0.25 Gs		

#### Table 2 Drive specifications summary for 4TB, 3TB & 2TB model (continued)

Drive Specification*	ST4000DM004	ST3000DM007	ST2000DM005	
Vibration, non-operating		5Hz to 22Hz: 3.0 Gs 22Hz to 350Hz: 3.0 Gs 350Hz to 500Hz: 3.0 Gs	I	
Drive acoustics, sound power				
Idle***		2.2 bels (typical) 2.4 bels (max)		
Seek		2.4 bels (typical) 2.7 bels (max)		
Non-recoverable read errors		1 per 10 <sup>14</sup> bits read		
Rated workload	Average annualized workload rating: <55 TB/year. The specifications for the product assumes the I/O workload does not exceed the average annualized workload rate limit of 55 TB/year. Workloads exceeding the annualized rate may degrade and impact reliability as experienced by the particular application. The average annualized workload rate limit is in units of TB per calendar year.			
Warranty	To determine the warranty for a specific drive, use a web browser to access the following web page: http://www.seagate.com/support/warranty-and-replacements/ From this page, click on "Is my Drive under Warranty". Users will be asked to provide the drive serial number, model number (or part number) and country of purchase. The system will display the warranty information for the drive.			
Load/unload cycles		600,000 at 25°C, 50% rel. humidity		
Supports hotplug operation per the Serial ATA Revision 3.2 specification	Yes			

\* All specifications above are based on native configurations.

\*\* One GB equals one billion bytes and 1TB equals one trillion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

\*\*\* During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

 Seagate does not recommend operating at sustained case temperatures above 60°C. Operating at higher temperatures will reduce useful life of the product.

	If the drive is powered-off before issuing flush cache command, in some instances,
Note	the end user data in the DRAM cache might not be committed to the disk.

# 2.2 Formatted capacity

Model	Formatted capacity*	Guaranteed sectors	Bytes per sector
8TB	8000GB	15,628,053,168	
6TB	6000GB	11,721,045,168	
4TB	4000GB	7,814,037,168	4096
3TB	3000GB	5,860,533,168	
2TB	2000GB	3,907,029,168	

\*One GB equals one billion bytes and 1TB equals one trillion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

# 2.2.1 LBA mode

When addressing these drives in LBA mode, all blocks (sectors) are consecutively numbered from 0 to n-1, where n is the number of guaranteed sectors as defined above.

See **Section 4.3.1, "Identify Device command"** (words 60-61 and 100-103) for additional information about 48-bit addressing support of drives with capacities over 137GB.

# 2.3 Default logical geometry

- Cylinders: 16,383
- Read/write heads: 16
- Sectors per track: 63

#### LBA mode

When addressing these drives in LBA mode, all blocks (sectors) are consecutively numbered from 0 to n-1, where n is the number of guaranteed sectors as defined above.

# 2.4 Recording and interface technology

Interface	SATA
Recording technology	SMR
Recording density (kBPI)	2294
Track density (ktracks/inch avg)	540
Areal density (Gb/in <sup>2</sup> )	1203
Interface transfer rate (MB/s)	600
Data transfer rate (MB/s)	up to 190

# 2.5 Physical characteristics

Maximum height	
8TB & 6TB	26.1mm / 1.028 in
4TB, 3TB & 2TB	20.20mm / 0.795 in
Maximum width	101.6mm / 4.0 in (± 0.010 in)
Maximum length	146.99mm / 5.787 in
Typical weight	
8TB	630g / 1.389 lb
6ТВ	610g / 1.345 lb
4TB, 3TB& 2TB	490g / 1.08 lb
Cache buffer	256MB

# 2.6 Start/stop times

The start/stop times are listed below.

Standard models	ST8000DM004, ST6000DM003 (4 and 3-Disk)	ST4000DM004, ST3000DM007 (3 and 2-Disk)	ST2000DM005 (2 and 1-Disk)
Power-on to ready (in seconds)	15 (typical)	10 (typical)	10 (typical)
Standby to ready (in seconds)	15 (typical)	10 (typical)	10 (typical)
Ready to spindle stop (in seconds)	18 (typical)	12 (typical)	10 (typical)

Time-to-ready may be longer than normal if the drive power is removed without going through normal OS powerdown procedures.

# 2.7 Power specifications

The drive receives DC power (+5V or +12V) through a native SATA power connector. Refer to Figure 2 on page 19.

# 2.7.1 Power consumption

Power requirements for the drives are listed in Table 3 and Table 4. Typical power measurements are based on an average of drives tested, under nominal conditions, using 5.0V and 12.0V input voltage at 25°C ambient temperature. These power measurements are done with DIPM enabled.

- Spinup current is measured from the time of power-on to the time that the drive spindle reaches operating speed.
- Read/Write current is measured with the heads on track, based on three 64 sector read or write operations every 100 ms.
- The drive supports three idle modes: Performance Idle mode, Active Idle mode and Low Power Idle mode. Refer to Section 2.7.4 for power-management modes.

Power dissipation	Avg (watts 25° C)	Avg 5V typ amps	Avg 12V typ amps
Spinup	_	_	2.0
Idle, Low Power	3.4	0.10	0.242
Read/Write	5.3	0.28	0.325
Standby	0.25	0.04	0.004
Sleep	0.25	0.04	0.004

Table 3 DC power requirements for 8TB and 6TB models

#### Table 4DC power requirements for 4TB, 3TB and 2TB models

Power dissipation	Avg (watts 25° C)	Avg 5V typ amps	Avg 12V typ amps
Spinup	_		2.0
Idle, Low Power	2.5	0.12	0.15
Read/Write	3.7	0.28	0.191
Standby	0.25	0.04	0.004
Sleep	0.25	0.04	0.004

# 2.7.2 Conducted noise

Input noise ripple is measured at the host system power supply across an equivalent 80-ohm resistive load on the +12 volt line or an equivalent 15-ohm resistive load on the +5 volt line.

- Using 12-volt power, the drive is expected to operate with a maximum of 120 mV peak-to-peak square-wave injected noise at up to 10MHz.
- Using 5-volt power, the drive is expected to operate with a maximum of 100 mV peak-to-peak square-wave injected noise at up to 10MHz.

**Note** Equivalent resistance is calculated by dividing the nominal voltage by the typical RMS read/write current.

# 2.7.3 Voltage tolerance

Voltage tolerance (including noise):

- 5VDC ±5%
- 12VDC ±10%

# 2.7.4 Power-management modes

The drive provides programmable power management to provide greater energy efficiency. In most systems, users can control power management through the system setup program. The drive features the following power-management modes:

Power modes	Heads	Spindle	Electronics
Active	Tracking	Rotating	Full Power
Idle, Performance	Tracking	Rotating	Full Power
Idle, Active	Floating	Rotating	Partial Power
Idle, Low Power	Parked	Rotating	Partial Power
Standby	Parked	Stopped	Low Power
Sleep	Parked	Stopped	Low Power

#### Active mode

The drive is in Active mode during the read/write and seek operations.

#### Idle mode

The electronics remain powered, and the drive accepts all commands and returns to Active mode when disk access is necessary.

#### Standby mode

The drive enters Standby mode immediately when the host sends a Standby Immediate command. If the host has set the standby timer, the drive enters Standby mode automatically after the drive has been inactive for a specifiable length of time. The standby timer delay is established using a Standby or Idle command. In Standby mode, the electronics are in low power mode, the heads are parked and the spindle is at rest. The drive accepts all commands and returns to Active mode when disk access is necessary.

#### Sleep mode

The drive enters Sleep mode after receiving a Sleep command from the host. In Sleep mode, the electronics are in low power mode, the heads are parked and the spindle is at rest. The drive leaves Sleep mode after it receives a Hard Reset or Soft Reset from the host. After receiving a reset, the drive exits Sleep mode and enters Standby mode.

#### Idle and Standby timers

Each time the drive performs an Active function (read, write or seek), the standby timer is reinitialized and begins counting down from its specified delay times to zero. If the standby timer reaches zero before any drive activity is required, the drive makes a transition to Standby mode. In both Idle and Standby mode, the drive accepts all commands and returns to Active mode when disk access is necessary.

# 2.8 Environmental specifications

This section provides the temperature, humidity, shock, and vibration specifications for BarraCuda drives. Ambient temperature is defined as the temperature of the environment immediately surrounding the drive. Above 1000ft. (305 meters), the maximum temperature is derated linearly by 1°C every 1000 ft. Refer to Section 3.4 on page 20 for base plate measurement location.

# 2.8.1 Ambient Temperature

Non-operating (Ambient)	–40° to 70°C (–40° to 158°F)	
Operating ambient (min °C)	0° (32°F)	
Operating (Drive case max °C)	60° (140°F) <sup>†</sup>	

+ Seagate does not recommend operating at sustained case temperatures above 60°C. Operating at higher temperatures will reduce useful life of the product.

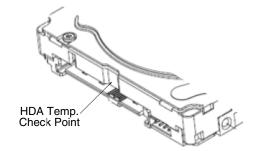


Figure 1 Location of the HDA temperature check point

Note	Image is for reference only, may not represent actual drive
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#### 2.8.2 Temperature gradient

Operating	20°C per hour (68°F per hour max), without condensation
Non-operating	30°C per hour (54°F per hour max)

## 2.8.3 Humidity

#### 2.8.3.1 Relative humidity

Operating	5% to 90% non-condensing (30% per hour max)
non-operating	5% to 95% non-condensing (30% per hour max)

#### 2.8.3.2 Wet bulb temperature

Operating	30°C / 86°F (rated)
Non-operating	40°C / 104°F (rated)

# 2.8.4 Altitude

Operating	–304m to 3048m (–1000 ft. to 10,000 ft.)
Non-operating	-304m to 12,192m (-1000 ft. to 40,000+ ft.)

# 2.8.5 Shock

All shock specifications assume that the drive is mounted securely with the input shock applied at the drive mounting screws. Shock may be applied in the X, Y or Z axis.

# 2.8.5.1 Operating shock

These drives comply with the performance levels specified in this document when subjected to a maximum operating shock of 80 Gs (read) / 70 Gs (write) based on half-sine shock pulses of 2ms during read operations. Shocks should not be repeated more than two times per second.

#### 2.8.5.2 Non-operating shock

#### 8TB, 6TB, 4TB & 3TB

The non-operating shock level that the drive can experience without incurring physical damage or degradation in performance when subsequently put into operation is 300 Gs based on a non-repetitive half-sine shock pulse of 2ms duration.

#### 2TB

The non-operating shock level that the drive can experience without incurring physical damage or degradation in performance when subsequently put into operation is 350 Gs based on a non-repetitive half-sine shock pulse of 2ms duration.

#### 2.8.5.3 Operating vibration

The maximum vibration levels that the drive may experience while meeting the performance standards specified in this document are specified below.

10Hz to 22Hz	0.25 Gs (Limited displacement)
22Hz to 350Hz	0.50 Gs
350Hz to 500Hz	0.25 Gs

All vibration specifications assume that the drive is mounted securely with the input vibration applied at the drive mounting screws. Vibration may be applied in the X, Y or Z axis. Throughput may vary if improperly mounted.

# 2.8.6 Non-operating vibration

The maximum non-operating vibration levels that the drive may experience without incurring physical damage or degradation in performance when subsequently put into operation are specified below.

5Hz to 22Hz	3.0 Gs (Limited displacement)
22Hz to 350Hz	3.0 Gs
350Hz to 500Hz	3.0 Gs

# 2.9 Acoustics

Drive acoustics are measured as overall A-weighted acoustic sound power levels (no pure tones). All measurements are consistent with ISO document 7779. Sound power measurements are taken under essentially free-field conditions over a reflecting plane. For all tests, the drive is oriented with the cover facing upward.

Note	

For seek mode tests, the drive is placed in seek mode only. The number of seeks per second is defined by the following equation: (Number of seeks per second = 0.4 / (average latency + average access time

#### Table 5 Fluid Dynamic Bearing (FDB) motor acoustics

	ldle*	Seek
8TB & 6TB models	2.3 bels (typical) 2.5 bels (max)	2.5 bels (typical) 2.6 bels (max)
4TB, 3TB & 2TB models	2.2 bels (typical) 2.4 bels (max)	2.4 bels (typical) 2.7 bels (max)

\*During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

# 2.9.1 Test for Prominent Discrete Tones (PDTs)

Seagate follows the ECMA-74 standards for measurement and identification of PDTs. An exception to this process is the use of the absolute threshold of hearing. Seagate uses this threshold curve (originated in ISO 389-7) to discern tone audibility and to compensate for the inaudible components of sound prior to computation of tone ratios according to Annex D of the ECMA-74 standards.

# 2.10 Electromagnetic immunity

When properly installed in a representative host system, the drive operates without errors or degradation in performance when subjected to the radio frequency (RF) environments defined in Table 6.

Test	Description	Performance level	Reference standard
Electrostatic discharge	Contact, HCP, VCP: ± 4 kV; Air: ± 8 kV	В	EN61000-4-2:95
Radiated RF immunity	80MHz to 1,000MHz, 3 V/m, 80% AM with 1kHz sine 900MHz, 3 V/m, 50% pulse modulation @ 200Hz	A	EN61000-4-3: 96 ENV50204: 95
Electrical fast transient	$\pm$ 1 kV on AC mains, $\pm$ 0.5 kV on external I/O	В	EN61000-4-4: 95
Surge immunity	± 1 kV differential, ± 2 kV common, AC mains	В	EN61000-4-5: 95
Conducted RF immunity	150kHz to 80MHz, 3 Vrms, 80% AM with 1kHz sine	А	EN61000-4-6: 97
Voltage dips, interrupts	0% open, 5 seconds 0% short, 5 seconds 40%, 0.10 seconds 70%, 0.01 seconds	C C C B	EN61000-4-11: 94

Table 6	Radio free	auencv	environments
14010 0		,,	

# 2.11 Warranty

To determine the warranty for a specific drive, use a web browser to access the following web page: <u>http://www.seagate.com/support/warranty-and-replacements/</u>

From this page, click on "Is my Drive under Warranty". Users will be asked to provide the drive serial number, model number (or part number) and country of purchase. The system will display the warranty information for the drive.

# 2.11.1 Data loss under power interruption with write cache enabled

Drive preserves its data during all operations except in cases where power to the drive is interrupted during write operations. This could result in either an uncorrected data error being reported, or the entire sector/track becoming unreadable. This can be permanently recovered by rewriting to the same location on the drive. Additionally any data present in the DRAM buffer will not be written to the disk media, additionally, the drive will not be able to return the original data.

In order to prevent this data loss, the host should issue a standby immediate or flush cache command before a controlled power off operation to the drive.

# 2.11.2 Storage

Maximum storage periods are 180 days within original unopened Seagate shipping package or 60 days unpackaged within the defined non-operating limits (refer to environmental section in this manual). Storage can be extended to 1 year packaged or unpackaged under optimal environmental conditions (25°C, <40% relative humidity non-condensing, and non-corrosive environment). During any storage period the drive non-operational temperature, humidity, wet bulb, atmospheric conditions, shock, vibration, magnetic and electrical field specifications should be followed.

# 2.12 HDD and SSD Regulatory Compliance and Safety

For the latest regulatory and compliance information see: https://www.seagate.com/support/ scroll to bottom of page and click the Seagate HDD and SSD Regulatory Compliance and Safety link.

#### 2.12.1 Regulatory models

The following regulatory model number represent all features and configurations within the series: Regulatory Model Numbers: SKR004 = 8TB & 6TB

SKR004 – 81B & 01B SKR002 = 4TB, 3TB & 2TB

# 2.13 Corrosive environment

Seagate electronic drive components pass accelerated corrosion testing equivalent to 10 years exposure to light industrial environments containing sulfurous gases, chlorine and nitric oxide, classes G and H per ASTM B845. However, this accelerated testing cannot duplicate every potential application environment. Users should use caution exposing any electronic components to uncontrolled chemical pollutants and corrosive chemicals as electronic drive component reliability can be affected by the installation environment. The silver, copper, nickel and gold films used in Seagate products are especially sensitive to the presence of sulfide, chloride, and nitrate contaminants. Sulfur is found to be the most damaging. In addition, electronic components should never be exposed to condensing water on the surface of the printed circuit board assembly (PCBA) or exposed to an ambient relative humidity greater than 95%. Materials used in cabinet fabrication, such as vulcanized rubber, that can outgas corrosive compounds should be minimized or eliminated. The useful life of any electronic equipment may be extended by replacing materials near circuitry with sulfide-free alternatives.

# 3.0 Configuring and Mounting the Drive

This section contains the specifications and instructions for configuring and mounting the drive.

# 3.1 Handling and static-discharge precautions

After unpacking, and before installation, the drive may be exposed to potential handling and electrostatic discharge (ESD) hazards. Observe the following standard handling and static-discharge precautions:

#### Caution

- Before handling the drive, put on a grounded wrist strap, or ground oneself frequently by touching the metal chassis of a computer that is plugged into a grounded outlet. Wear a grounded wrist strap throughout the entire installation procedure.
- Handle the drive by its edges or frame *only*.
- The drive is extremely fragile—handle it with care. Do not press down on the drive top cover.
- Always rest the drive on a padded, antistatic surface until users mount it in the computer.
- Do not touch the connector pins or the printed circuit board.
- Do not remove the factory-installed labels from the drive or cover them with additional labels. Removal voids the warranty. Some factory-installed labels contain information needed to service the drive. Other labels are used to seal out dirt and contamination.

# 3.2 Configuring the drive

Each drive on the SATA interface connects point-to-point with the SATA host adapter. There is no master/slave relationship because each drive is considered a master in a point-to-point relationship. If two drives are attached on one SATA host adapter, the host operating system views the two devices as if they were both "masters" on two separate ports. Both drives behave as if they are Device 0 (master) devices.

SATA drives are designed for easy installation. It is usually not necessary to set any jumpers on the drive for proper operation; however, if users connect the drive and receive a "drive not detected" error, the SATA-equipped motherboard or host adapter may use a chipset that does not support SATA speed autonegotiation.

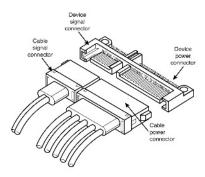
# 3.3 SATA cables and connectors

The SATA interface cable consists of four conductors in two differential pairs, plus three ground connections. The cable size may be 30 to 26 AWG with a maximum length of one meter (39.37 inches). See **Table 7** for connector pin definitions. Either end of the SATA signal cable can be attached to the drive or host.

For direct backplane connection, the drive connectors are inserted directly into the host receptacle. The drive and the host receptacle incorporate features that enable the direct connection to be hot pluggable and blind mateable.

For installations which require cables, users can connect the drive as illustrated in Figure 2.

#### Figure 2 Attaching SATA cabling



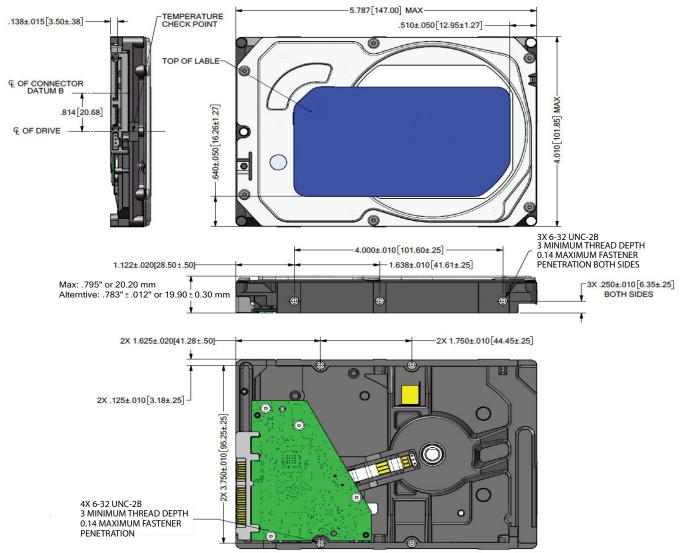
Each cable is keyed to ensure correct orientation. BarraCuda drives support latching SATA connectors.

# 3.4 Drive mounting

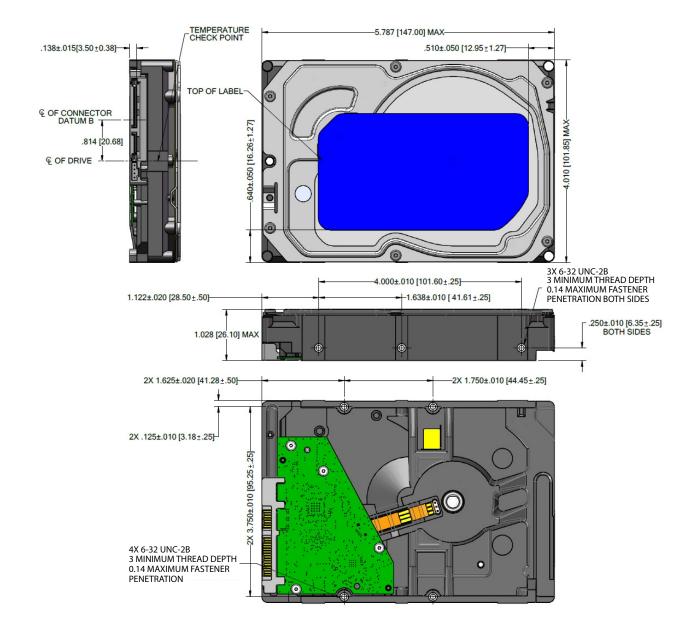
Users can mount the drive in any orientation using four screws in the side-mounting holes or four screws in the bottom-mounting holes. Refer to Figure 3 for drive mounting dimensions. Follow these important mounting precautions when mounting the drive:

- Allow a minimum clearance of 0.030 inches (0.76mm) around the entire perimeter of the drive for cooling.
- Use only 6-32 UNC mounting screws.
- The screws should be inserted no more than 0.140 inch (3.56 mm) into the bottom or side mounting holes.
- Do not overtighten the mounting screws (maximum torque: 6 inch-lb).

#### Figure 3 Mounting dimensions (1 and 2-disk models)



#### Figure 4 Mounting dimensions (3 and 4-disk models)



# 4.0 SATA Interface

These drives use the industry-standard Serial ATA (SATA) interface that supports FIS data transfers. It supports ATA programmed input/output (PIO) modes 0 to 4; multiword DMA modes 0 to 2, and Ultra DMA modes 0 to 6.

For detailed information about the SATA interface, refer to the "Serial ATA: High Speed Serialized AT Attachment" specification.

# 4.1 Hot-Plug compatibility

BarraCuda drives incorporate connectors which enable users to hot plug these drives in accordance with the SATA Revision 3.2 specification. This specification can be downloaded from <u>www.serialata.org</u>.

# 4.2 SATA device plug connector pin definitions

Table 7 summarizes the signals on the SATA interface and power connectors.

Segment	Pin	Function	Definition
Signal	S1	Ground	2nd mate
	S2	A+	<ul> <li>Differential signal pair A from Phy</li> </ul>
	S3	A-	
	S4	Ground	2nd mate
	S5	В-	<ul> <li>Differential signal pair B from Phy</li> </ul>
	S6	B+	
	S7	Ground	2nd mate
Key and spa	acing sep	arate signal and power se	gments
Power	P1	V33	3.3V power
	P2	V33	3.3V power
	P3	V33	3.3V power, pre-charge, 2nd mate
	P4	Ground	1st mate
	P5	Ground	2nd mate
	P6	Ground	2nd mate
	P7	V5	5V power, pre-charge, 2nd mate
	P8	V5	5V power
	P9	V5	5V power
	P10	Ground	2nd mate
	P11	Ground or LED signal	If grounded, drive does not use deferred spin
	P12	Ground	1st mate.
	P13	V12	12V power, pre-charge, 2nd mate
	P14	V12	12V power
	P15	V12	12V power

#### Table 7 SATA connector pin definitions

#### Notes

2.

- 1. All pins are in a single row, with a 1.27 mm (0.050 in) pitch.
  - The comments on the mating sequence apply to the case of backplane blindmate connector only. In this case, the mating sequences are:
    - the ground pins P4 and P12.
    - the pre-charge power pins and the other ground pins.
    - the signal pins and the rest of the power pins.
- 3. There are three power pins for each voltage. One pin from each voltage is used for pre-charge when installed in a blind-mate backplane configuration.
  - All used voltage pins (V<sub>x</sub>) must be terminated.

# 4.3 Supported ATA commands

The following table lists SATA standard commands that the drive supports. For a detailed description of the ATA commands, refer to the Serial ATA International Organization: Serial ATA Revision 3.0 (<u>http://www.sata-io.org</u>).

See "S.M.A.R.T. commands" on page 31 for details and subcommands used in the S.M.A.R.T. implementation.

#### Table 8 SATA standard commands **Command name** Command code (in hex) **Check Power Mode** E5<sub>H</sub> **Device Configuration Freeze Lock** B1<sub>H</sub> / C1<sub>H</sub> **Device Configuration Identify** B1<sub>H</sub> / C2<sub>H</sub> **Device Configuration Restore** B1<sub>H</sub> / C0<sub>H</sub> **Device Configuration Set** B1<sub>H</sub> / C3<sub>H</sub> **Device Reset** 08<sub>H</sub> Download Microcode 92<sub>H</sub> **Execute Device Diagnostics** 90<sub>H</sub> Flush Cache E7<sub>H</sub> Flush Cache Extended EA<sub>H</sub> Format Track 50<sub>H</sub> Identify Device ECH Idle E3<sub>H</sub> Idle Immediate E1<sub>H</sub> Initialize Device Parameters 91<sub>H</sub> **Read Buffer** E4<sub>H</sub> Read DMA C8<sub>H</sub> Read DMA Extended 25<sub>H</sub> **Read DMA Without Retries** C9<sub>H</sub> Read Log Ext $2F_{H}$ **Read Multiple** C4<sub>H</sub> **Read Multiple Extended** 29<sub>H</sub> Read Native Max Address F8<sub>H</sub> Read Native Max Address Extended 27<sub>H</sub> **Read Sectors** 20<sub>H</sub> **Read Sectors Extended** 24<sub>H</sub> **Read Sectors Without Retries** 21<sub>H</sub> **Read Verify Sectors** 40<sub>H</sub> **Read Verify Sectors Extended** 42<sub>H</sub> **Read Verify Sectors Without Retries** 41<sub>H</sub> Recalibrate 10<sub>H</sub> Security Disable Password F6<sub>H</sub> Security Erase Prepare F3<sub>H</sub> Security Erase Unit F4<sub>H</sub> Security Freeze F5<sub>H</sub> Security Set Password F1<sub>H</sub> Security Unlock F2<sub>H</sub>

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Seek     70 <sub>H</sub> Set Features     FF <sub>H</sub> Set Max Address     F9 <sub>H</sub> Set Max Address     Pdferss:     00H       Inder Individual Set Max Address     00H       ormands are identified by the value placed in the Set Max Features registers and defined to the right.     03H       Set Max Address Extended     37 <sub>H</sub> Set Max Address Extended     66 <sub>H</sub> Set Max Induces Extended     66 <sub>H</sub> SMAR.T. Disable Operations     80 <sub>H</sub> /D9 <sub>H</sub> SMAR.T. Enable/Disable Autosave     80 <sub>H</sub> /D9 <sub>H</sub> SMAR.T. Enable Operations     80 <sub>H</sub> /D4 <sub>H</sub> SMAR.T. Enable Operations     80 <sub>H</sub> /D4 <sub>H</sub> SMAR.T. Read Data     80 <sub>H</sub> /D4 <sub>H</sub> SMAR.T. Read	Command name	Command code (in hex)
Set Max Address     F9 <sub>H</sub> Note: Individual Set Max Address commands are identified by the value defined to the right.     OOH Password: O2H Unlock: 02H Unlock: 03H Freeze Lock:     OOH O2H Unlock: 03H Freeze Lock:       Set Max Address Extended     37, E       Set Max Address Extended     37, E       Set Max Address Extended     37, E       Set Max Address Extended     66, E       Set Max Address Extended     80, P0, P0, E       SMAR.T. Enable/Disable Autosave     B0, P0, P0, P0, E       SMAR.T. Enable/Disable Autosave     B0, P0, P0, E       SMAR.T. Enable/Disable Autosave     B0, P0, P0, P0, E       SMAR.T. Enable/Disable Autosave     B0, P0, P0, E       SMAR.T. Execute Offline     B0, P0, P0, E       SMAR.T. Read Attribute Thresholds     B0, P0, P0, E       SMAR.T. Read Data     B0, P0, P0, E       SMAR.T. Write Log Sector     B0, P0, P0, E       Standby     E2, E       Standby     E4, E       Write DMA     CA, E       Write DMA     CA, E       Write DMA     S5, E       Write DMA     S5, E       Write Multiple     S5, E	Seek	70 <sub>H</sub>
Note: Individual Set Max Address commands are identified by the value placed in the Set Max Factures register as defined to the right.Address: Prezez Lock:OOH DIH Lock: 02H DistrictOOH Presez Lock:OOH DIH DistrictDistrictOOH DIH DistrictDistrictOOH DIH District	Set Features	EF <sub>H</sub>
placed in the Set Max Reatures register as defined to the right.Password: Data Dit Precez Lock: D4HD1H D4HSet Max Address Extended37,4Set Max Address Extended66,4Set Multiple ModeC6,4SMAR.T. Disable OperationsB0,4 / D9,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D2,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D2,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D2,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D2,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D3,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D1,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D3,4S.M.A.R.T. Enable/Disable AutosaveB0,4 / D3,4S.M.A.R.T. Execut OfflineB0,4 / D3,4S.M.A.R.T. Read Attribute ThresholdsB0,4 / D3,4S.M.A.R.T. Read Log SectorB0,4 / D3,4S.M.A.R.T. Save Attribute ValuesB0,4 / D6,4S.M.A.R.T. Save Attribute ValuesB0,4 / D6,4S.M.A.R.T. Save Attribute ValuesB0,4 / D6,4S.M.A.R.T. Save Attribute ValuesB0,4 / D6,4StandbyE2,4StandbyE2,4StandbyE3,4Write DMA Fue ExtendedG5,4Write DMA Fue ExtendedG2,4Write DMA Fue ExtendedG2,4Write DMA Fue ExtendedG3,4Write Multiple ExtendedG4,4Write Multiple ExtendedG4,4Write Multiple ExtendedG4,4Write Multiple ExtendedG4,4Write Multiple ExtendedG4,4 </td <td>Set Max Address</td> <td>F9<sub>H</sub></td>	Set Max Address	F9 <sub>H</sub>
Set Multiple Mode         C6 <sub>H</sub> Sleep         E6 <sub>H</sub> SMAR.T. Disable Operations         B0 <sub>H</sub> / D9 <sub>H</sub> SMAR.T. Enable/Disable Autosave         B0 <sub>H</sub> / D2 <sub>H</sub> SMAR.T. Enable/Disable Autosave         B0 <sub>H</sub> / D2 <sub>H</sub> SMAR.T. Enable/Disable Autosave         B0 <sub>H</sub> / D2 <sub>H</sub> SMAR.T. Execute Offline         B0 <sub>H</sub> / D4 <sub>H</sub> SMAR.T. Read Attribute Thresholds         B0 <sub>H</sub> / D1 <sub>H</sub> SMAR.T. Read Data         B0 <sub>H</sub> / D5 <sub>H</sub> SMAR.T. Read Log Sector         B0 <sub>H</sub> / D5 <sub>H</sub> SMAR.T. Read Log Sector         B0 <sub>H</sub> / D3 <sub>H</sub> SMAR.T. Save Attribute Values         B0 <sub>H</sub> / D6 <sub>H</sub> SMAR.T. Save Attribute Values         B0 <sub>H</sub> / D6 <sub>H</sub> Standby         E2 <sub>H</sub> Standby Immediate         E0 <sub>H</sub> Write Buffer         E8 <sub>H</sub> Write DMA         CA <sub>H</sub> Write DMA FUA Extended         3D <sub>H</sub> Write DMA Without Retries         CB <sub>H</sub> Write DMA Without Retries         CB <sub>H</sub> Write Multiple Extended         3P <sub>H</sub> Write Multiple Extended         SP <sub>H</sub> Write Multiple FUA Extended         CE <sub>H</sub> Write Multiple FUA Extended         CE	commands are identified by the value placed in the Set Max Features register as	Password:01HLock:02HUnlock:03H
SleepE6HS.M.A.R.T. Disable OperationsB0H / D9HS.M.A.R.T. Enable/Disable AutosaveB0H / D2HS.M.A.R.T. Enable/Disable AutosaveB0H / D8HS.M.A.R.T. Enable OperationsB0H / D4HS.M.A.R.T. Execute OfflineB0H / D4HS.M.A.R.T. Read Attribute ThresholdsB0H / D0HS.M.A.R.T. Read DataB0H / D5HS.M.A.R.T. Read DataB0H / D5HS.M.A.R.T. Read Log SectorB0H / D3HS.M.A.R.T. Reat Log SectorB0H / D3HS.M.A.R.T. Save Attribute ValuesB0H / D6HS.M.A.R.T. Write Log SectorB0H / D6HStandbyE2HStandby ImmediateE0HWrite BufferE8HWrite DMACAHWrite DMACAHWrite DMA FUA Extended35HWrite DMA FUA Extended35HWrite DMA Without RetriesCBHWrite MultipleCSHWrite Multiple FUA Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Extended31HWrite Sectors Extended34H	Set Max Address Extended	37 <sub>H</sub>
SMA.R.T. Disable OperationsB0H / D9HSMA.R.T. Enable/Disable AutosaveB0H / D2HSMA.R.T. Enable/Disable AutosaveB0H / D8HSMA.R.T. Enable OperationsB0H / D4HSMA.R.T. Execute OfflineB0H / D0HSMA.R.T. Read Attribute ThresholdsB0H / D0HSMA.R.T. Read DataB0H / D0HSMA.R.T. Read Log SectorB0H / D5HSMA.R.T. Read Log SectorB0H / D3HSMA.R.T. Save Attribute ValuesB0H / D6HSMA.R.T. Save Attribute ValuesB0H / D6HSMA.R.T. Write Log SectorB0H / D6HStandbyE2HStandbyE2HStandbyE2HStandbyE3HWrite BufferE8HWrite DMACAHWrite DMASHWrite DMACAHWrite DMA Extended35HWrite DMA Uthout RetriesCBHWrite UnitipleC5HWrite MultipleSHWrite Multiple FUA Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Set Multiple Mode	C6 <sub>H</sub>
S.M.A.R.T. Enable/Disable AutosaveB0 <sub>H</sub> / D2 <sub>H</sub> S.M.A.R.T. Enable OperationsB0 <sub>H</sub> / D8 <sub>H</sub> S.M.A.R.T. Execute OfflineB0 <sub>H</sub> / D4 <sub>H</sub> S.M.A.R.T. Execute OfflineB0 <sub>H</sub> / D0 <sub>H</sub> S.M.A.R.T. Read Attribute ThresholdsB0 <sub>H</sub> / D0 <sub>H</sub> S.M.A.R.T. Read DataB0 <sub>H</sub> / D5 <sub>H</sub> S.M.A.R.T. Read Log SectorB0 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Save Attribute ValuesB0 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Save Attribute ValuesB0 <sub>H</sub> / D6 <sub>H</sub> S.M.A.R.T. Write Log SectorB0 <sub>H</sub> / D6 <sub>H</sub> StandbyE2 <sub>H</sub> StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write DMACA <sub>H</sub> Write DMAS5 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Uthout RetriesCB <sub>H</sub> Write DMA Uthout RetriesCB <sub>H</sub> Write MultipleC5 <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Ktended39 <sub>H</sub> Write Multiple FUA ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	Sleep	E6 <sub>H</sub>
SMAR.T. Enable OperationsB0H / D8HS.M.A.R.T. Execute OfflineB0H / D4HS.M.A.R.T. Execute OfflineB0H / D4HS.M.A.R.T. Read Attribute ThresholdsB0H / D0HS.M.A.R.T. Read DataB0H / D0HS.M.A.R.T. Read Log SectorB0H / D5HS.M.A.R.T. Read Log SectorB0H / DAHS.M.A.R.T. Save Attribute ValuesB0H / D3HS.M.A.R.T. Wite Log SectorB0H / D6HStandbyE2HStandbyE2HWrite BufferE8HWrite DMACAHWrite DMASAHWrite DMACAHWrite DMA Extended35HWrite DMA FUA Extended35HWrite DMA Extended35HWrite MultipleC5HWrite Multiple Extended39HWrite Multiple Extended26HWrite Multiple FUA Extended26HWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	S.M.A.R.T. Disable Operations	B0 <sub>H</sub> / D9 <sub>H</sub>
S.M.A.R.T. Execute OfflineB0 <sub>H</sub> / D4 <sub>H</sub> S.M.A.R.T. Read Attribute ThresholdsB0 <sub>H</sub> / D0 <sub>H</sub> S.M.A.R.T. Read DataB0 <sub>H</sub> / D0 <sub>H</sub> S.M.A.R.T. Read Log SectorB0 <sub>H</sub> / D5 <sub>H</sub> S.M.A.R.T. Reat Log SectorB0 <sub>H</sub> / DA <sub>H</sub> S.M.A.R.T. Save Attribute ValuesB0 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Write Log SectorB0 <sub>H</sub> / D6 <sub>H</sub> StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write BufferE8 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA Extended30 <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write DMA Without RetriesS1 <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Multiple FUA Extended30 <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Strended34 <sub>H</sub>	S.M.A.R.T. Enable/Disable Autosave	B0 <sub>H</sub> / D2 <sub>H</sub>
S.M.A.R.T. Read Attribute ThresholdsB0H / D1HS.M.A.R.T. Read DataB0H / D0HS.M.A.R.T. Read Log SectorB0H / D5HS.M.A.R.T. Read Log SectorB0H / DAHS.M.A.R.T. Return StatusB0H / DAHS.M.A.R.T. Save Attribute ValuesB0H / D6HS.M.A.R.T. Virite Log SectorB0H / D6HStandbyE2HStandbyE0HWrite BufferE8HWrite DMACAHWrite DMAS5HWrite DMA Extended35HWrite DMA FUA Extended35HWrite DMA Without RetriesCBHWrite DMA Without RetriesC5HWrite MultipleS5HWrite Multiple Extended39HWrite Multiple ExtendedS9HWrite Sectors30HWrite Sectors Without Retries30HWrite Sectors Without Retries30HWrite Sectors Without Retries30HWrite Sectors Stended31HWrite Sectors Stended34H	S.M.A.R.T. Enable Operations	B0 <sub>H</sub> / D8 <sub>H</sub>
SMA.R.T. Read DataB0H / D0HS.M.A.R.T. Read Log SectorB0H / D5HS.M.A.R.T. Return StatusB0H / DAHS.M.A.R.T. Save Attribute ValuesB0H / D3HS.M.A.R.T. Write Log SectorB0H / D6HStandbyE2HStandbyE0HWrite BufferE8HWrite DMACAHWrite DMASAHWrite DMA Extended35HWrite Log Extended30HWrite Log ExtendedSHWrite DMA FUA Extended3FHWrite DMA Without RetriesC8HWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors Without Retries30HWrite Sectors Without Retries31HWrite Sectors Extended34H	S.M.A.R.T. Execute Offline	B0 <sub>H</sub> / D4 <sub>H</sub>
S.M.A.R.T. Read Log SectorB0 <sub>H</sub> / D5 <sub>H</sub> S.M.A.R.T. Return StatusB0 <sub>H</sub> / DA <sub>H</sub> S.M.A.R.T. Save Attribute ValuesB0 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Write Log SectorB0 <sub>H</sub> / D6 <sub>H</sub> StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write BufferE8 <sub>H</sub> Write DMACA <sub>H</sub> Write DMASD <sub>H</sub> Write DMASD <sub>H</sub> Write DMASD <sub>H</sub> Write DMA Extended3D <sub>H</sub> Write DMA FUA ExtendedSD <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Sectors Without Retries30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Without Retries34 <sub>H</sub>	S.M.A.R.T. Read Attribute Thresholds	B0 <sub>H</sub> / D1 <sub>H</sub>
S.M.A.R.T. Return Status $BO_H / DA_H$ S.M.A.R.T. Save Attribute Values $BO_H / D3_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby Immediate $EO_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $3D_H$ Write Log Extended $3F_H$ Write Iufiple $C5_H$ Write Multiple Extended $39_H$ Write Multiple FUA Extended $30_H$ Write Sectors $30_H$	S.M.A.R.T. Read Data	B0 <sub>H</sub> / D0 <sub>H</sub>
S.M.A.R.T. Save Attribute ValuesB0 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Write Log SectorB0 <sub>H</sub> / D6 <sub>H</sub> StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write BufferE8 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA Extended3D <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	S.M.A.R.T. Read Log Sector	B0 <sub>H</sub> / D5 <sub>H</sub>
S.M.A.R.T. Write Log SectorB0 <sub>H</sub> / D6 <sub>H</sub> StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write BufferE8 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA Extended3D <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple FUA Extended39 <sub>H</sub> Write Sectors Without Retries30 <sub>H</sub> Write Sectors Extended31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	S.M.A.R.T. Return Status	B0 <sub>H</sub> / DA <sub>H</sub>
StandbyE2HStandby ImmediateE0HWrite BufferE8HWrite DMACAHWrite DMA Extended35HWrite DMA FUA Extended3DHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple FUA Extended39HWrite Sectors Without Retries30HWrite Sectors Extended31HWrite Sectors Extended34H	S.M.A.R.T. Save Attribute Values	B0 <sub>H</sub> / D3 <sub>H</sub>
Standby ImmediateE0HWrite BufferE8HWrite DMACAHWrite DMA Extended35HWrite DMA FUA Extended3DHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	S.M.A.R.T. Write Log Sector	B0 <sub>H</sub> / D6 <sub>H</sub>
Write BufferE8Write DMACAWrite DMA Extended35Write DMA FUA Extended3DWrite DMA FUA Extended3DWrite DMA Without RetriesCBWrite Log Extended3FWrite MultipleC5Write Multiple Extended39Write Sectors Without Retries30Write Sectors Extended31Write Sectors Extended34	Standby	E2 <sub>H</sub>
Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA Extended3D <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Multiple FUA ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	Standby Immediate	EO <sub>H</sub>
Write DMA Extended $35_{H}$ Write DMA FUA Extended $3D_{H}$ Write DMA Without Retries $CB_{H}$ Write Log Extended $3F_{H}$ Write Multiple $C5_{H}$ Write Multiple Extended $39_{H}$ Write Multiple FUA Extended $CE_{H}$ Write Sectors Without Retries $31_{H}$ Write Sectors Extended $34_{H}$	Write Buffer	E8 <sub>H</sub>
Write DMA FUA Extended3DHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write DMA	CA <sub>H</sub>
Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Multiple FUA ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	Write DMA Extended	35 <sub>H</sub>
Write Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write DMA FUA Extended	3D <sub>H</sub>
Write MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write DMA Without Retries	CB <sub>H</sub>
Write Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write Log Extended	3F <sub>H</sub>
Write Multiple FUA Extended     CE <sub>H</sub> Write Sectors     30 <sub>H</sub> Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple	C5 <sub>H</sub>
Write Sectors     30 <sub>H</sub> Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple Extended	39 <sub>H</sub>
Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple FUA Extended	CE <sub>H</sub>
Write Sectors Extended     34 <sub>H</sub>	Write Sectors	30 <sub>H</sub>
	Write Sectors Without Retries	31 <sub>H</sub>
Write Uncorrectable 45	Write Sectors Extended	34 <sub>H</sub>
<u></u>	Write Uncorrectable	45 <sub>H</sub>

#### Table 8 SATA standard commands (continued)

# 4.3.1 Identify Device command

The Identify Device command (command code EC<sub>H</sub>) transfers information about the drive to the host following power up. The data is organized as a single 512-byte block of data, whose contents are shown in **"Supported ATA commands" on page 23**. All reserved bits or words should be set to zero. Parameters listed with an "x" are drive-specific or vary with the state of the drive.

The following commands contain drive-specific features that may not be included in the SATA specification.

Table 9	Identify Device commands		
Word	Description	Value	
0	Configuration information: • Bit 15: 0 = ATA; 1 = ATAPI • Bit 7: removable media • Bit 6: removable controller • Bit 0: reserved	0C5A <sub>H</sub>	
1	Number of logical cylinders	16,383	
2	<ul> <li>Specific configuration:</li> <li>37C8h Device requires SET FEATURES subcommand to spin-up after power-up and IDENTIFY DEVICE data is incomplete.</li> <li>738Ch Device requires SET FEATURES subcommand to spin-up after power-up and IDENTIFY DEVICE data is complete.</li> <li>8C73h Device does not require SET FEATURES subcommand to spin-up after power-up and IDENTIFY DEVICE data is incomplete.</li> <li>C837h Device does not require SET FEATURES subcommand to spin-up after power-up and IDENTIFY DEVICE data is incomplete.</li> </ul>	C837 <sub>H</sub>	
3	Number of logical heads	16	
4	Retired	0000 <sub>H</sub>	
5	Retired	0000 <sub>H</sub>	
6	Number of logical sectors per logical track: 63	003F <sub>H</sub>	
7–9	Retired	0000 <sub>H</sub>	
10–19	Serial number: (20 ASCII characters, 0000 <sub>H</sub> = none)	ASCII	
20	Retired	0000 <sub>H</sub>	
21	Retired	0400 <sub>H</sub>	
22	Obsolete	0000 <sub>H</sub>	
23–26	Firmware revision (8 ASCII character string, padded with blanks to end of string)	x.xx	
27–46	Drive model number: (40 ASCII characters, padded with blanks to end of string)		
47	(Bits 7–0) Maximum sectors per interrupt on Read multiple and Write multiple (16)	8010 <sub>H</sub>	
48	Trusted Computing feature set options: 15 Shall be cleared to zero 14 Shall be set to one 13:1 Reserved for the Trusted Computing Group 0 Trusted Computing feature set is supported	4000 <sub>H</sub>	
49	Standard Standby timer, IORDY supported and may be disabled	2F00 <sub>H</sub>	

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## Table 9 Identify Device commands (continued)

Word	Description	Value
	Capabilities: (see 7.17.7.17)	
50	15 Shall be cleared to zero 14 Shall be set to one 13:2 Reserved 1 Obsolete 0 Shall be set to one to indicate a vendor specific Standby timer value minimum	4000 <sub>H</sub>
51	PIO data-transfer cycle timing mode	0200 <sub>H</sub>
52	Retired (Obsolete)	0200 <sub>H</sub>
53	<ul> <li>15:8 Free-fall Control Sensitivity</li> <li>7:3 Reserved</li> <li>2 the fields reported in word 88 are valid</li> <li>1 the fields reported in words (70:64) are valid</li> <li>0 Obsolete</li> </ul>	0007 <sub>H</sub>
54	Number of current logical cylinders (Obsolete)	xxxx <sub>H</sub>
55	Number of current logical heads (Obsolete)	xxxx <sub>H</sub>
56	Number of current logical sectors per logical track (Obsolete)	xxxx <sub>H</sub>
57–58	Current capacity in sectors (Obsolete)	xxxx <sub>H</sub>
59	<ul> <li>15 The BLOCK ERASE EXT command is supported</li> <li>14 The OVERWRITE EXT command is supported</li> <li>13 The CRYPTO SCRAMBLE EXT command is supported</li> <li>12 The Sanitize feature set is supported</li> <li>11:9 Reserved</li> <li>8 Multiple logical sector setting is valid</li> <li>7:0 Current setting for number of logical sectors that shall be transferred per DRQ data block on READ/WRITE Multiple commands</li> </ul>	5C10 <sub>H</sub>
60–61	Total number of user-addressable LBA sectors available (see <b>Section 2.2</b> for related information) *Note: The maximum value allowed in this field is: 0FFFFFFh (268,435,455 sectors, 137GB). Drives with capacities over 137GB will have 0FFFFFFh in this field and the actual number of user-addressable LBAs specified in words 100-103. This is required for drives that support the 48-bit addressing feature.	0FFFFFFh*
62	Obsolete	0000 <sub>H</sub>
63	Multiword DMA active and modes supported (see note following this table)	<i>xx</i> 07 <sub>H</sub>
64	Advanced PIO modes supported (modes 3 and 4 supported)	0003 <sub>H</sub>
65	Minimum multiword DMA transfer cycle time per word (120 nsec)	0078 <sub>H</sub>
66	Recommended multiword DMA transfer cycle time per word (120 nsec)	0078 <sub>H</sub>
67	Minimum PIO cycle time without IORDY flow control (240 nsec)	0078 <sub>H</sub>
68	Minimum PIO cycle time with IORDY flow control (120 nsec)	0078 <sub>H</sub>

## Table 9 Identify Device commands (continued)

Word	Description	Value
69	Additional Supported 15 CFast Specification Support 14 Deterministic data in trimmed LBA range(s) is supported 13 Long Physical Sector Alignment Error Reporting Control is supported 12 Obsolete 11 READ BUFFER DMA is supported 10 WRITE BUFFER DMA is supported 9 Obsolete 8 DOWNLOAD MICROCODE DMA is supported 7 Reserved for IEEE 1667 6 0 = Optional ATA device 28-bit commands supported 5 Trimmed LBA range(s) returning zeroed data is supported 4 Device Encrypts All User Data 3 Extended Number of User Addressable Sectors is supported 2 All write cache is non-volatile 1:0 Reserved	0000 <sub>H</sub>
70–74	ATA-reserved	0000 <sub>H</sub>
75	Queue depth	001F <sub>H</sub>
76	SATA capabilities	xxxx <sub>H</sub>
77	Reserved for future SATA definition	xxxx <sub>H</sub>
78	SATA features supported	xxxx <sub>H</sub>
79	SATA features enabled	xxxx <sub>H</sub>
80	Major version number	07F0 <sub>H</sub>
81	Minor version number	006D <sub>H</sub>
82	Command sets supported	306B <sub>H</sub>
83	Command sets supported	7561 <sub>H</sub>
84	Command sets support extension (see note following this table)	4163 <sub>H</sub>
85	Command sets enabled	30xx <sub>H</sub>
86	Command sets enabled	B441 <sub>H</sub>
87	Command sets enable extension	6173 <sub>H</sub>
88	Ultra DMA support and current mode (see note following this table)	xx7F <sub>H</sub>
89	Security erase time	xxxx <sub>H</sub>
90	Enhanced security erase time	xxxx <sub>H</sub>
92	Master password revision code	FFFE <sub>H</sub>
93	Hardware reset value	xxxx <sub>H</sub>
94	Automatic acoustic management	D0D0 <sub>H</sub>
95–99	ATA-reserved	0000 <sub>H</sub>
100–103	Total number of user-addressable LBA sectors available (see <b>Section 2.2</b> for related information). These words are required for drives that support the 48-bit addressing feature. Maximum value: 0000FFFFFFFFFFFFF.	ST8000DM004 = 15,628,053,168 ST6000DM003 = 11,721,045,168 ST4000DM004 = 7,814,037,168 ST3000DM007 = 5,860,533,168 ST2000DM005 = 3,907,029,168
104-105	ATA-reserved	0000 <sub>H</sub>

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#### Table 9 Identify Device commands (continued)

Word	Description	Value
106	Physical sector size / logical sector size	6003 <sub>H</sub>
107	ATA-reserved	0000 <sub>H</sub>
108-111	The mandatory value of the world wide name (WWN) for the drive. <b>NOTE:</b> This field is valid if word 84, bit 8 is set to 1 indicating 64-bit WWN support.	Each drive will have a unique value.
112-118	ATA-reserved	0000 <sub>H</sub>
119	Commands and feature sets supported	41DE <sub>H</sub>
120	Commands and feature sets supported or enabled	409C <sub>H</sub>
121-127	ATA-reserved	0000 <sub>H</sub>
128	Security status	0021 <sub>H</sub>
129–159	Seagate-reserved	xxxx <sub>H</sub>
160–167	ATA-reserved	0000 <sub>H</sub>
168	Device Nominal Form Factor	0002 <sub>H</sub>
169-205	ATA-reserved	0000 <sub>H</sub>
206	SCT Command Transport	10A5 <sub>H</sub>
207-208	ATA-reserved	0000 <sub>H</sub>
209	Alignment of logical blocks within a physical block	4000 <sub>H</sub>
210-216	ATA-reserved	0000 <sub>H</sub>
217	Nominal media rotation rate	175C <sub>H</sub>
218-221	ATA-reserved	0000 <sub>H</sub>
222	Transport major version number	107F <sub>H</sub>
223-229	ATA-reserved	0000 <sub>H</sub>
230-233	Extended Number of User Addressable Sectors	ST8000DM004 = 15,628,053,168 ST6000DM003 = 11,721,045,168 ST4000DM004 = 7,814,037,168 ST3000DM007 = 5,860,533,168 ST2000DM005 = 3,907,029,168
234–254	ATA-reserved	0000 <sub>H</sub>
255	Integrity word	xxA5 <sub>H</sub>

Note

Advanced Power Management (APM) and Automatic Acoustic Management (AAM) features are not supported.

Note

See the bit descriptions below for words 63, 84, and 88 of the Identify Drive data.

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Descri	Description (if bit is set to 1)				
	Bit	Word 63			
	0	Multiword DMA mode 0 is supported.			
	1	Multiword DMA mode 1 is supported.			
	2	Multiword DMA mode 2 is supported.			
	8	Multiword DMA mode 0 is currently active.			
	9	Multiword DMA mode 1 is currently active.			
	10	Multiword DMA mode 2 is currently active.			
	Bit	Word 84			
	0	SMART error login is supported.			
	1	SMART self-test is supported.			
	2	Media serial number is supported.			
	3	Media Card Pass Through Command feature set is supported.			
	4	Streaming feature set is supported.			
	5	GPL feature set is supported.			
	6	WRITE DMA FUA EXT and WRITE MULTIPLE FUA EXT commands are supported.			
	7	WRITE DMA QUEUED FUA EXT command is supported.			
	8	64-bit World Wide Name is supported.			
	9-10	Obsolete.			
	11-12	Reserved for TLC.			
	13	IDLE IMMEDIATE command with IUNLOAD feature is supported.			
	14	Shall be set to 1.			
	15	Shall be cleared to 0.			
	Bit	Word 88			
	0	Ultra DMA mode 0 is supported.			
	1	Ultra DMA mode 1 is supported.			
	2	Ultra DMA mode 2 is supported.			
	3	Ultra DMA mode 3 is supported.			
	4	Ultra DMA mode 4 is supported.			
	5	Ultra DMA mode 5 is supported.			
	6	Ultra DMA mode 6 is supported.			
	8	Ultra DMA mode 0 is currently active.			
	9	Ultra DMA mode 1 is currently active.			
	10	Ultra DMA mode 2 is currently active.			
	11	Ultra DMA mode 3 is currently active.			
	12	Ultra DMA mode 4 is currently active.			
	13	Ultra DMA mode 5 is currently active.			
	14	Ultra DMA mode 6 is currently active.			
-					

# 4.3.2 Set Features command

This command controls the implementation of various features that the drive supports. When the drive receives this command, it sets BSY, checks the contents of the Features register, clears BSY and generates an interrupt. If the value in the register does not represent a feature that the drive supports, the command is aborted. Power-on default has the read look-ahead and write caching features enabled. The acceptable values for the Features register are defined as follows:

Table 10	Set Features command
02 <sub>H</sub>	Enable write cache (default)
03 <sub>H</sub>	Set transfer mode (based on value in Sector Count register) Sector Count register values:
	00 <sub>H</sub> Set PIO mode to default (PIO mode 2)
	01 <sub>H</sub> Set PIO mode to default and disable IORDY (PIO mode 2)
	08 <sub>H</sub> PIO mode 0
	09 <sub>H</sub> PIO mode 1
	0A <sub>H</sub> PIO mode 2
	0B <sub>H</sub> PIO mode 3
	0C <sub>H</sub> PIO mode 4 ( <i>default</i> )
	20 <sub>H</sub> Multiword DMA mode 0
	21 <sub>H</sub> Multiword DMA mode 1
	22 <sub>H</sub> Multiword DMA mode 2
	40 <sub>H</sub> Ultra DMA mode 0
	41 <sub>H</sub> Ultra DMA mode 1
	42 <sub>H</sub> Ultra DMA mode 2
	43 <sub>H</sub> Ultra DMA mode 3
	44 <sub>H</sub> Ultra DMA mode 4
	45 <sub>H</sub> Ultra DMA mode 5
	46 <sub>H</sub> Ultra DMA mode 6
06 <sub>H</sub>	Enable the PUIS feature set
07 <sub>H</sub>	PUIS feature set device spin-up
10 <sub>H</sub>	Enable use of SATA features
55 <sub>H</sub>	Disable read look-ahead (read cache) feature
82 <sub>H</sub>	Disable write cache
86 <sub>H</sub>	Disable the PUIS feature set
90 <sub>H</sub>	Disable use of SATA features
AA <sub>H</sub>	Enable read look-ahead (read cache) feature (default)
F1 <sub>H</sub>	Report full capacity available

**Note** At power-on, or after a hardware or software reset, the default values of the features are as indicated above.

# 4.3.3 S.M.A.R.T. commands

S.M.A.R.T. provides near-term failure prediction for disk drives. When S.M.A.R.T. is enabled, the drive monitors predetermined drive attributes that are susceptible to degradation over time. If self-monitoring determines that a failure is likely, S.M.A.R.T. makes a status report available to the host. Not all failures are predictable. S.M.A.R.T. predictability is limited to the attributes the drive can monitor. For more information on S.M.A.R.T. commands and implementation, see the *Draft ATA-5 Standard*.

SeaTools diagnostic software activates a built-in drive self-test (DST S.M.A.R.T. command for D4<sub>H</sub>) that eliminates unnecessary drive returns. The diagnostic software ships with all new drives and is also available at: <u>http://seatools.seagate.com</u>.

This drive is shipped with S.M.A.R.T. features disabled. Users must have a recent BIOS or software package that supports S.M.A.R.T. to enable this feature. The table below shows the S.M.A.R.T. command codes that the drive uses.

Code in features register	S.M.A.R.T. command
D0 <sub>H</sub>	S.M.A.R.T. Read Data
D2 <sub>H</sub>	S.M.A.R.T. Enable/Disable Attribute Autosave
D3 <sub>H</sub>	S.M.A.R.T. Save Attribute Values
D4 <sub>H</sub>	S.M.A.R.T. Execute Off-line Immediate (runs DST)
D5 <sub>H</sub>	S.M.A.R.T. Read Log Sector
D6 <sub>H</sub>	S.M.A.R.T. Write Log Sector
D8 <sub>H</sub>	S.M.A.R.T. Enable Operations
D9 <sub>H</sub>	S.M.A.R.T. Disable Operations
DA <sub>H</sub>	S.M.A.R.T. Return Status

Table 11	S.M.A.R.T.	commands
14010111	J	commanas

Note	If an appropriate code is not written to the Features Register, the command is aborted and 0x04 (abort) is written to the Error register.



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Publication Number: 100805918, Rev. N July 2020

# tehnotzka

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Za najnovije informacije o ceni, dostupnim akcijama i tehničkim karakteristikama proizvoda koji se pominje u ovom dokumentu, molimo posetite našu stranicu klikom na sledeći link:

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